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Mekuria, F.; Fjallbrant, T.;
Circuits and Systems, 1990., IEEE International Symposium on
1-3 May 1990 Page(s):1883 - 1886 vol.3
Digital Object Identifier 10.1109/ISCAS.1990.112046

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Inventor Information for 10/649565

Inventor Name	City	State/Country
VANDANAPU, NAVEEN KUMAR	ANDHRA PRADESH	INDIA

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Application#	Patent#	Status	Date Filed	Title	Inventor Name
10649565	Not Issued	30	08/26/2003	Method for reducing data dependency in codebook searches for multi-ALU DSP architectures	VANDANAPU, NAVEEN KUMAR
10741304	Not Issued	120	12/18/2003	Bit allocation for encoding track information	VANDANAPU, NAVEEN KUMAR

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